



RCE/2800

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# REQUEST FOR CONTINUED EXAMINATION (RCE) TRANSMITTAL

Subsection (b) of 35 U.S.C. § 132, effective May 29, 2000 provides for continued examination of a utility or plant application filed on or after June 8, 1995.

See The American Inventors Protection Act of 1999 (AIPA).

## Complete if Known

Application Number	09/772,115
Filing Date	01/26/2001
First Named Inventor	Eric Lee
Group/Art Unit	2811
Examiner Name	Douglas W. Owens
Attorney Docket Number	IDT-1641(RCE)

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This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 for the above-identified application.

**NOTE:** 37 C.F.R. § 1.114 is effective on May 29, 2000. If the above-identified application was filed prior to May 29, 2000, applicant may wish to consider filing a continued prosecution application (CPA) under 37 C.F.R. § 1.53(d) (PTO/SB/29) instead of an RCE to be eligible for the patent term adjustment provisions of the AIPA. See, *Changes to Application Examination and Provisional Application Practice*, Interim Rule, 65 Fed. Re. 14865 (Mar. 20, 2000), 1233 Off. Gaz. Pat. Office 47 (Apr. 11, 2000), which established RCE practice.

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### 1. Submission Required under 37 C.F.R. § 1.114

- a. ☐ Previously submitted
- i. ☐ Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on (Any unentered amendment(s) referred to above will be entered).
- ii. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on
- iii. ☐ Other:
- b. ☒ Enclosed
- i. ☒ Amendment/Reply to Final Office Action dated May 3, 2002
- ii. ☐ Affidavit(s)/Declaration(s)
- iii. ☐ Information Disclosure Statement (IDS)
- iv. ☒ Other: Petition For Extension Of Time (2 months)

### 2. Miscellaneous

- a. ☐ Suspension of action on the above-identified application is request under 37 C.F.R. § 1.103(c) for a period of \_\_\_ months. (Period of suspension shall not exceed 3 months; Fee under 37 C.F.R. § 1.17(i) required.)
- b. ☐ Other:

### 3. Fees (The RCE fee under 37 C.F.R. § 1.17(e) is required by 37 C.F.R. § 1.114 when the RCE is filed.)

- a. ☒ The Director is hereby authorized to charge the following fees, or credit any overpayments, to Deposit Account No. 50-0574 (Docket No. IDT-1641(RCE))
- i. ☒ RCE fee required under 37 C.F.R. § 1.17(e) of \$740.00.
- ii. ☒ Extension of time fee - 2 months (37 C.F.R. §§ 1.136 and 1.17) of \$400.00.
- iii. ☐ Other:
- b. ☐ Check in the amount of \$ \_\_\_, enclosed.
- c. ☐ Payment by credit card (Form PTO-2038 enclosed).

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## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED

Name (Print/Type)	E. Eric Hoffman	Registration No. (Attorney/Agent)	38,186	Telephone	(925) 895-3545
Signature		Date	October 3, 2002		

## CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Box RCE, Washington, DC 20231, or facsimile transmitted to the U.S. Patent And Trademark Office on:

Name (Print/Type)	E. Eric Hoffman	Date	10-3-02
Signature			

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Eric Lee, Dave Cobert and Wanqing Cao  
Assignee: Integrated Device Technology, Inc.  
Title: CONFORMAL SURFACE SILICIDE STRAP ON SPACER  
Serial No: 09/772,115 Group No.: 2811  
Filed: 01/26/01 Examiner: D. Owens  
Attorney Docket No.: IDT-1641

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October 3, 2002

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RESPONSE TO FINAL OFFICE ACTION

Sir:

Applicants submit a Request For Continued Examination under 37 CFR 1.114, along with the following remarks and amendments in response to the Final Office Action dated May 3, 2002. A request for a 2-month extension, extending the time for response to October 3, 2002, accompanies the present response.

IN THE CLAIMS

Cancel Claims 8-18.

Add new Claims 26-38 as follows:

-- 26. The semiconductor structure of Claim 19, wherein the silicide strap comprises a refractory metal layer reacted with semiconductor material in the conductive element, the dielectric spacer and the semiconductor substrate.

27. The semiconductor structure of Claim 26, wherein the semiconductor material comprises amorphous silicon.

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